

# **PowerManager**<sup>™</sup>

# **General Description**

The AAT3520 Series of PowerManager™ products is a member of AnalogicTech™'s Total Power Management™ IC (TPM ICs™) product family. These microprocessor reset circuits are ideal for monitoring voltage supplies in portable systems, where extended battery life is critical. They provide a low cost, reliable solution by eliminating external components. The AAT3520 series operates by monitoring the system power supply voltage. When the input voltage drops below a fixed threshold, the device asserts a reset signal for a fixed time period after V<sub>CC</sub> has risen back above the fixed threshold; 30ms and 150ms minimum periods are available. The AAT3520 product series is guaranteed to operate down to 1.2V and designed to ignore fast line transients appearing on V<sub>CC</sub>. The AAT3520 series is available with three output stage versions: AAT3520 push-pull active high output, AAT3522 push-pull active low output and AAT3524 open drain active low output. The quiescent supply current is extremely low, typically 1µA, making it ideal for portable battery operated equipment.

The AAT3520/2/4 are available in a 3 pin SOT23 package and are specified over -40° to 85°C operating temperature range.

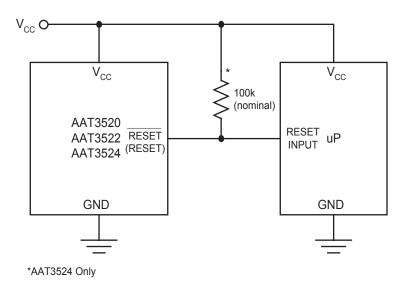
#### **Features**

- 1.2V to 5.5V Input voltage range
- Operation down to 1.2V
- Extremely low quiescent current: less than 2µA
- High accuracy detection threshold: ±1.5%
- Monitor Power Supply Voltages
- Fixed thresholds from 2.2V to 4.6V
- Reset pulse width options
  - Minimum 30 ms or 150ms
  - Fast propagation delay <20µs
- Available output configurations:
  - · Open-drain output
  - · CMOS active high output
  - CMOS active low output
- Temp range -40 to 85°C
- 3 pin SOT23 package
- 4kV ESD rating

### **Applications**

- Notebook Computers
- Cell Phones
- Portable Electronics
- Embedded Systems
- Intelligent Instruments

## **Typical Application**





# **Pin Descriptions**

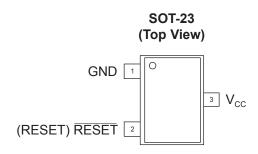
### AAT3520, 3522, 3524 (SOT23-3)

Pin #	Symbol	Function
1	GND	Ground connection
2	RESET (AAT3520)	RESET output remains high while $V_{CC}$ is below the reset threshold and remains so for a fixed time period after $V_{CC}$ raises above the reset threshold.
	RESET (AAT3522, AAT3524)	$\overline{\text{RESET}}$ output remains low while $V_{CC}$ is below the reset threshold and remains so for a fixed time period after $V_{CC}$ raises above the reset threshold
3	Vcc	Supply Voltage (+1.2V to +5.5V). Due to extremely low operating current it is recommended to place a 10nF capacitor between $V_{\rm CC}$ and GND.

# **Part Number Descriptions**

SOT23-3 Part Number	Part Description
AAT3520	Reset Output Push Pull Active High with Delay
AAT3522	Reset Output Push Pull Active Low with Delay
AAT3524	Reset Output Open Drain Active Low with Delay

# **Pin Configuration**



# AAT3520/2/4 MicroPower Microprocessor Reset Circuit

## **Absolute Maximum Ratings** (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Description	Value	Units
V <sub>CC</sub>	V <sub>CC</sub> to GND	-0.3 to 5.5	V
V <sub>RESET</sub>	RESET to GND (Push-Pull Output)	-0.3 to V <sub>CC</sub> +0.3	V
	RESET to GND (Open Drain Output)	-0.3 to 5.5	
I <sub>MAX</sub>	Maximum Continuous Input Current	20	mA
I <sub>RESET</sub>	RESET/RESET Current	20	mA
dVcc/dt	Rate of rise of Vcc	100	V/µs
T <sub>J</sub>	Operating Junction Temperature Range	-40 to 150	°C
T <sub>LEAD</sub>	Maximum Soldering Temperature (at Leads) for 10s	300	°C

Note: Stresses above those listed in Absolute Maximum Ratings may cause permanent damage to the device. Functional operation at conditions other than the operating conditions specified is not implied. Only one Absolute Maximum rating should be applied at any one time.

### **Thermal Information**

Symbol	Description	Value	Units
$\Theta_{JA}$	Maximum Thermal Resistance (SOT23-3) <sup>1</sup>	200	°C/W
P <sub>D</sub>	Maximum Power Dissipation (SOT23-3) <sup>1</sup>	320	mW

Note 1: Mounted on an FR4 board.



#### AAT3520/2/4

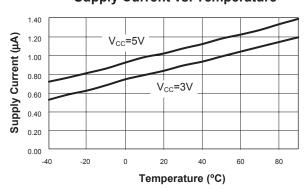
# **MicroPower Microprocessor Reset Circuit**

Symbol	Description	Conditions	Min	Тур	Max	Units	
\/	Operation Valtage	$T_A = 0$ °C to +70°C	1.0		5.5	V	
V <sub>cc</sub>	Operation Voltage	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	1.2		5.5	1 V	
	Quiescent Current	V <sub>CC</sub> = 5.5V		1.05	3	μА	
Ι <sub>Q</sub>	Quiescent Current	$V_{CC} = 3V$		0.85	2		
V	RESET Threshold (Table 1)	$T_A = 25^{\circ}C$	V <sub>TH</sub> -1.5%	$V_{TH}$	V <sub>TH</sub> +1.5%	V	
$V_{TH}$	RESET THRESHOLD (Table 1)	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	V <sub>TH</sub> -2.5%	$V_{TH}$	V <sub>TH</sub> +2.5%	V	
$\Delta V_{TH}/^{\circ}C$	RESET Threshold Tempco			40		ppm/°C	
t <sub>P</sub>	RESET Propagation Delay	$V_{CC} = V_{TH}$ to $(V_{TH} - 100 \text{mV})$		15		μs	
t	RESET Active Timeout	-50 option	30	50	100	ms	
t <sub>RDY</sub>	Period	-200 option	150	250	400	1113	
		$I_{SINK}$ =1.2mA, $V_{CC} = V_{TH(min)}$ ,			0.3		
V <sub>OL</sub>	RESET Low Output Voltage AAT3522, AAT3524	V <sub>TH</sub> ≤ 3.08V, RESET asserted				V	
▼ OL		$I_{SINK}$ =3.2mA, $V_{CC}$ = $V_{TH(min)}$ , $V_{TH}$ > 3.08V, RESET asserted			0.4		
.,	RESET High Output Voltage	$I_{SOURCE}$ = 800 $\mu$ A, $V_{TH}$ > 3.08V, $V_{CC}$ > $V_{TH (max)}$	V <sub>CC</sub> -1.5			V	
V <sub>OH</sub>	AAT3522	$I_{SOURCE}$ = 500 $\mu$ A, $V_{TH} \le 3.08V$ , $V_{CC} > V_{TH \text{ (max)}}$	0.8 V <sub>CC</sub>			V	
W	RESET Low Output Voltage AAT3520	$I_{SINK}$ =1.2mA, $V_{CC} > V_{TH (max)}$ , $V_{TH} \le 3.08V$ , RESET not asserted			0.3	V	
V <sub>OL</sub>		$I_{SINK}$ = 3.2mA, $V_{CC} > V_{TH (max)}$ , $V_{TH} >$ 3.08V, RESET not asserted			0.4		
V <sub>OH</sub>	RESET High Output Voltage	I <sub>SOURCE</sub> = 500μA, V <sub>CC</sub> > 2.1V, RESET asserted	0.8V <sub>CC</sub>			V	
	AAT3520	I <sub>SOURCE</sub> = 50μA, V <sub>CC</sub> > 1.2V, RESET asserted	0.8V <sub>CC</sub>			v	
I <sub>DOFF</sub>	RESET leakage current, AAT3524	V <sub>CC</sub> > V <sub>TH</sub>			1	μA	

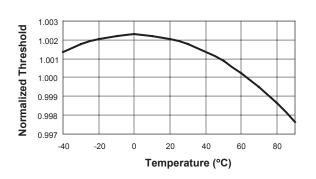


# **Typical Characteristics**

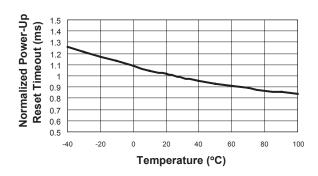
**Supply Current vs. Temperature** 



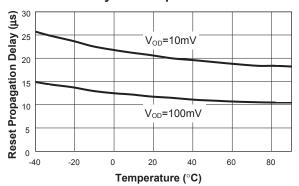
Normalized Reset Threshold vs. Temperature



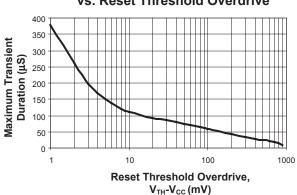
Normalized Power-Up Reset Timeout vs. Temperature



Power-Down Reset Propagation Delay vs. Temperature

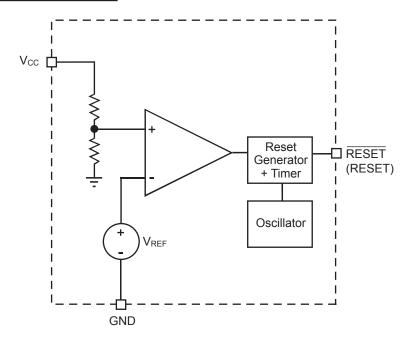


Maximum Transient Duration vs. Reset Threshold Overdrive





# **Functional Block Diagram**



# **Functional Description**

#### General

During start-up, the reset pin on a microprocessor ensures that it is fully reset and starts up in a known condition. The AAT3520 series of microprocessor reset devices monitor the supply voltage to a microprocessor and assert a reset signal whenever the  $V_{\rm CC}$  voltage falls below a factory programmed threshold. This threshold is accurate within +/-1.5% at 25°C and within +/- 2.5% over the entire operating temperature range. The reset signal remains asserted for a fixed time period ( $t_{\rm RDY}$ ) after  $V_{\rm CC}$  has risen above the threshold as shown in Figure 1. See Ordering Information for available RESET active timeout periods

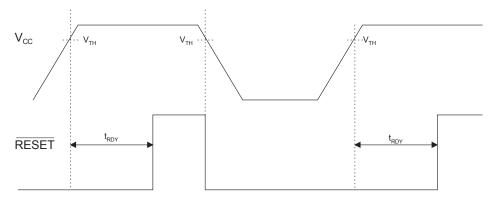


Figure 1: Reset Timing Diagram (AAT3522, AAT3524)

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### **Applications Information**

#### **Reset Output Options**

The AAT3524 has an active low open drain output and the AAT3520/2 have a push-pull output. The AAT3524 may be used in multiple voltage systems, with an external pull-up resistor. This allows the AAT3524 to interface to microprocessors with higher supply voltages up to 5.5V (Figure 3). In the event of a power down or brown-out condition the reset signal remains valid until the  $V_{\rm CC}$  drops below 1.2V. To ensure validity down to ground an external  $100 k\Omega$  resistor should be connected between the RESET output and GND.

#### **Supply Voltage Transient Behavior**

In some cases fast negative transients of short duration can appear on the  $V_{CC}$  power supply. The AAT3520 series devices provide some immunity to line transients which can generate invalid reset pulses. Figure 1 shows typical behavior to short duration pulses verses RESET comparator overdrive. As shown in the Maximum Transient Duration vs. Reset Threshold Overdrive graph, when the transient voltage becomes larger, the time allowed before asserting a reset becomes shorter. e.g. typically a transient of 100mV below the reset threshold would have to present for more than 50 $\mu$ s to cause a reset. Immunity can be increased by the addition of a small by-pass capacitor of  $0.1\mu$ F connected as close to the  $V_{CC}$  pin as possible.

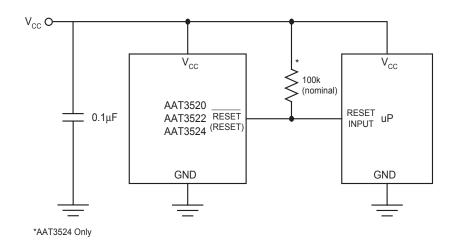


Figure 2: Using Recommended Bypass Capacitor

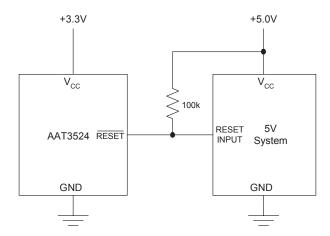


Figure 3: Using AAT3524 Open Drain Output with Multiple Supplies



# Factory Trimmed Reset Thresholds\* and Ordering Information Table 1

			Reset Threshold Voltage, V <sub>TH</sub> (V)				(V)	
Ordering	Mini	Minimum		T <sub>A</sub> = 25°C			T <sub>A</sub> = 40 to 85°C	
Part Number	Marking	Reset Active (ms)	Min	Тур	Max	Min	Max	
AAT3522IGY-2.20-200-T1	DB	150	2.167	2.200	2.233	2.145	2.255	
AAT3520IGY-2.32-200-T1 <b>AAT3522IGY-2.32-200-T1</b> AAT3524IGY-2.32-200-T1	DS DT	150 150 150	2.285	2.320	2.355	2.262	2.378	
AAT3522IGY-2.45-200-T1	DX	150	2.413	2.450	2.487	2.389	2.511	
AAT3520IGY-2.63-200-T1 AAT3522IGY-2.63-200-T1 AAT3524IGY-2.63-200-T1	GN CA CW	150 150 150	2.591	2.630	2.669	2.564	2.696	
AAT3520IGY-2.93-200-T1 AAT3522IGY-2.93-50-T1 AAT3522IGY-2.93-200-T1 AAT3524IGY-2.93-200-T1	CZ FV CC FP	150 30 150 150	2.886	2.930	2.974	2.857	3.003	
AAT3520IGY-3.08-50-T1 AAT3520IGY-3.08-200-T1 AAT3522IGY-3.08-50-T1 AAT3522IGY-3.08-200-T1 AAT3524IGY-3.08-200-T1	EF CN CY CB BG	30 150 30 150 150	3.034	3.080	3.126	3.003	3.157	
AAT3520IGY-4.38-200-T1 AAT3522IGY-4.38-200-T1 AAT3524IGY-4.38-200-T1	HJ FU IV	150 150 150	4.314	4.380	4.446	4.271	4.490	
AAT3520IGY-4.63-200-T1 AAT3522IGY-4.63-200-T1 AAT3524IGY-4.63-200-T1	HK DA DU	150 150 150	4.561	4.630	4.699	4.514	4.746	

Note: Sample stock is generally held on all part numbers listed in **BOLD**.

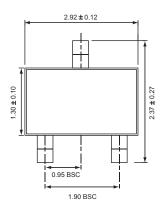
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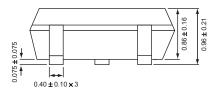
<sup>\*</sup>Contact Local Sales Office for custom trimmed options



# Package Information

#### SOT23-3







All dimensions in millimeters.



# AAT3520/2/4 MicroPower Microprocessor Reset Circuit

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